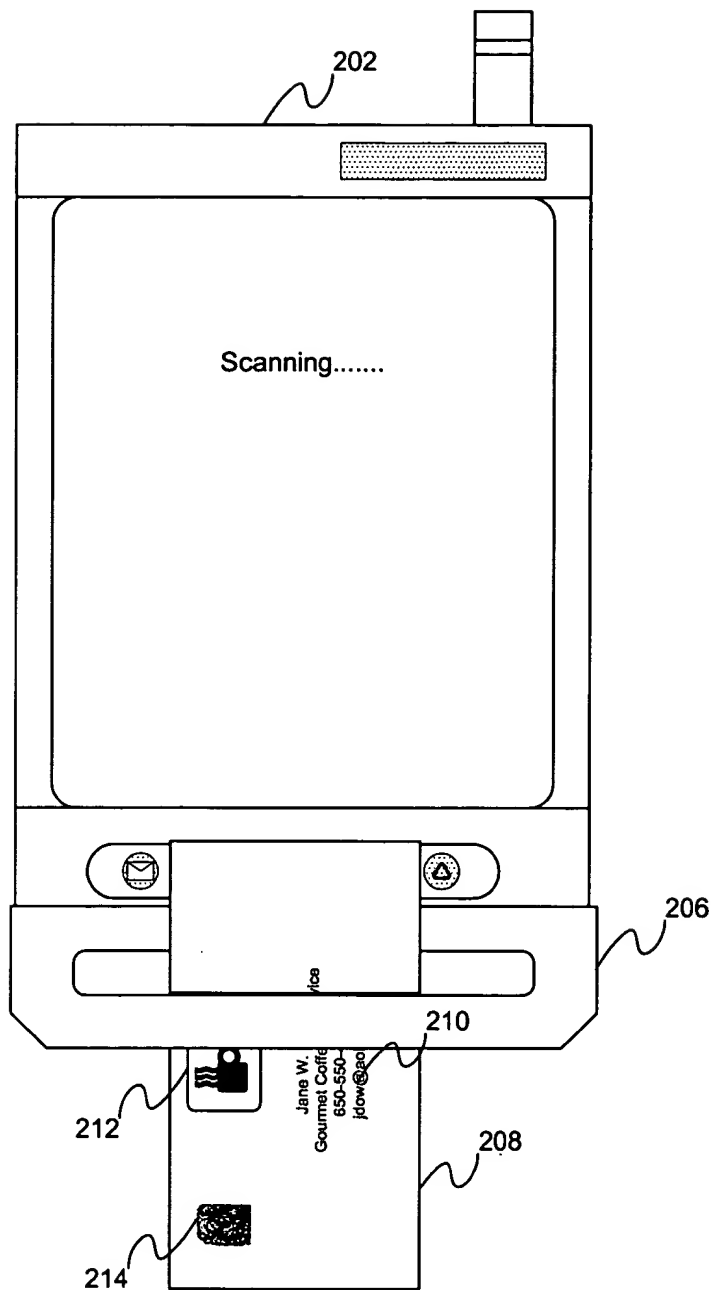
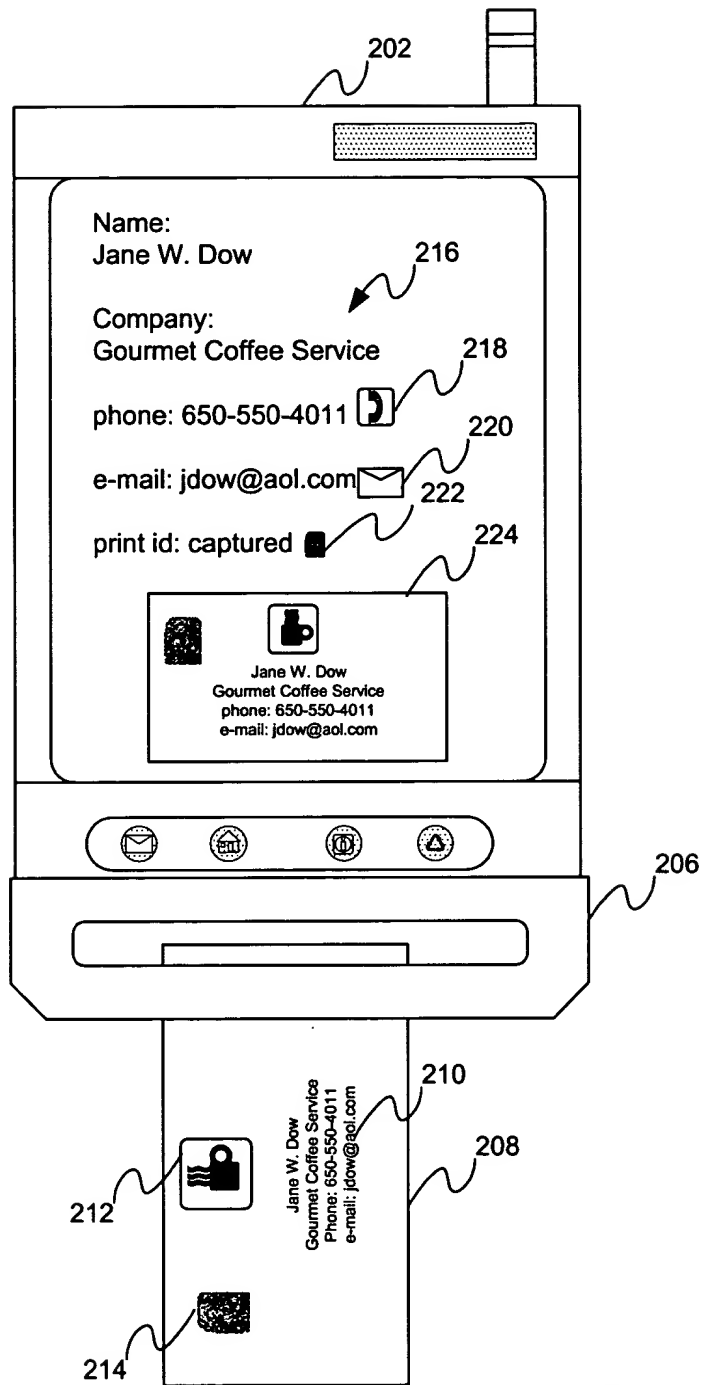


**Figure 1**

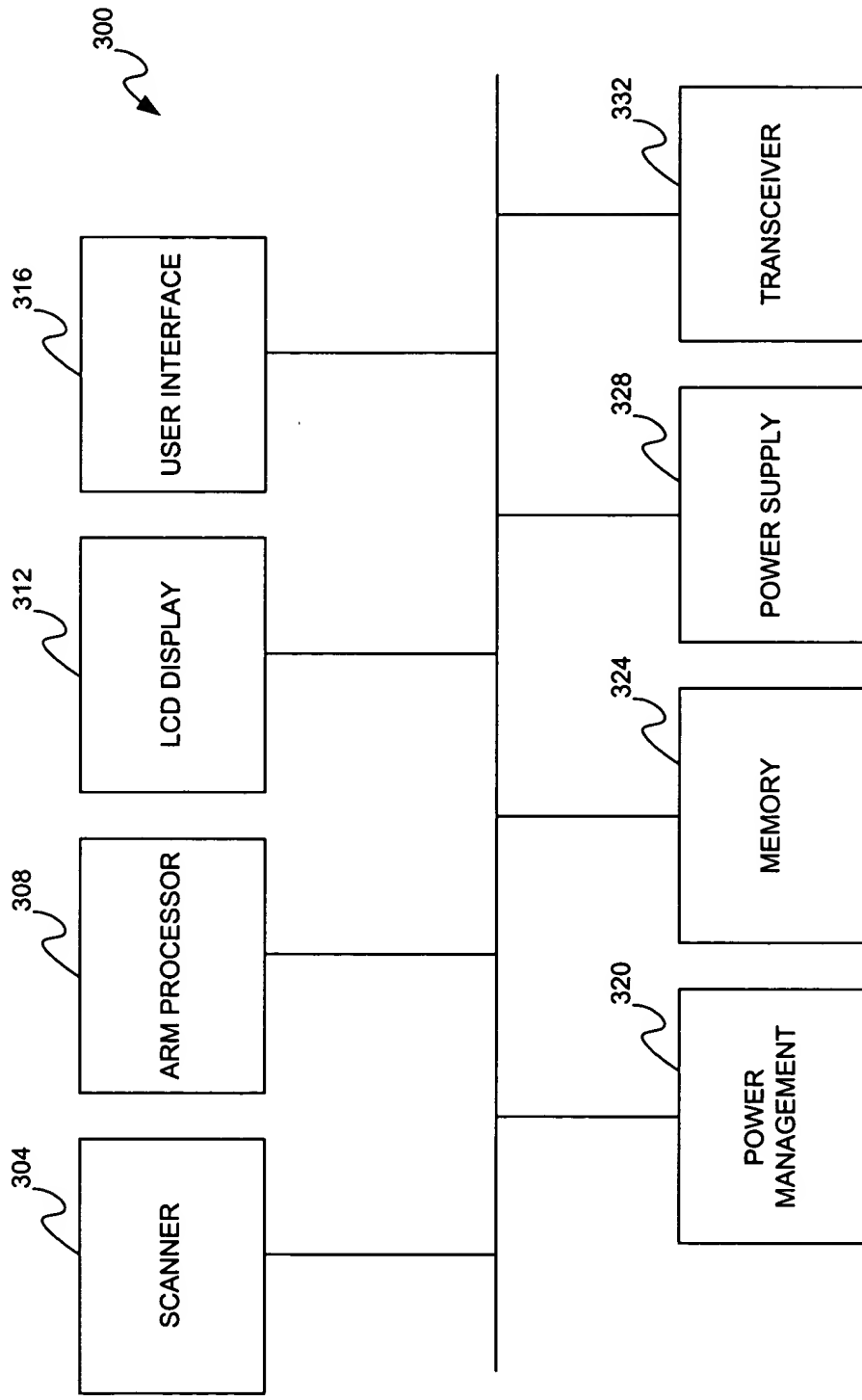


**Figure 2A**



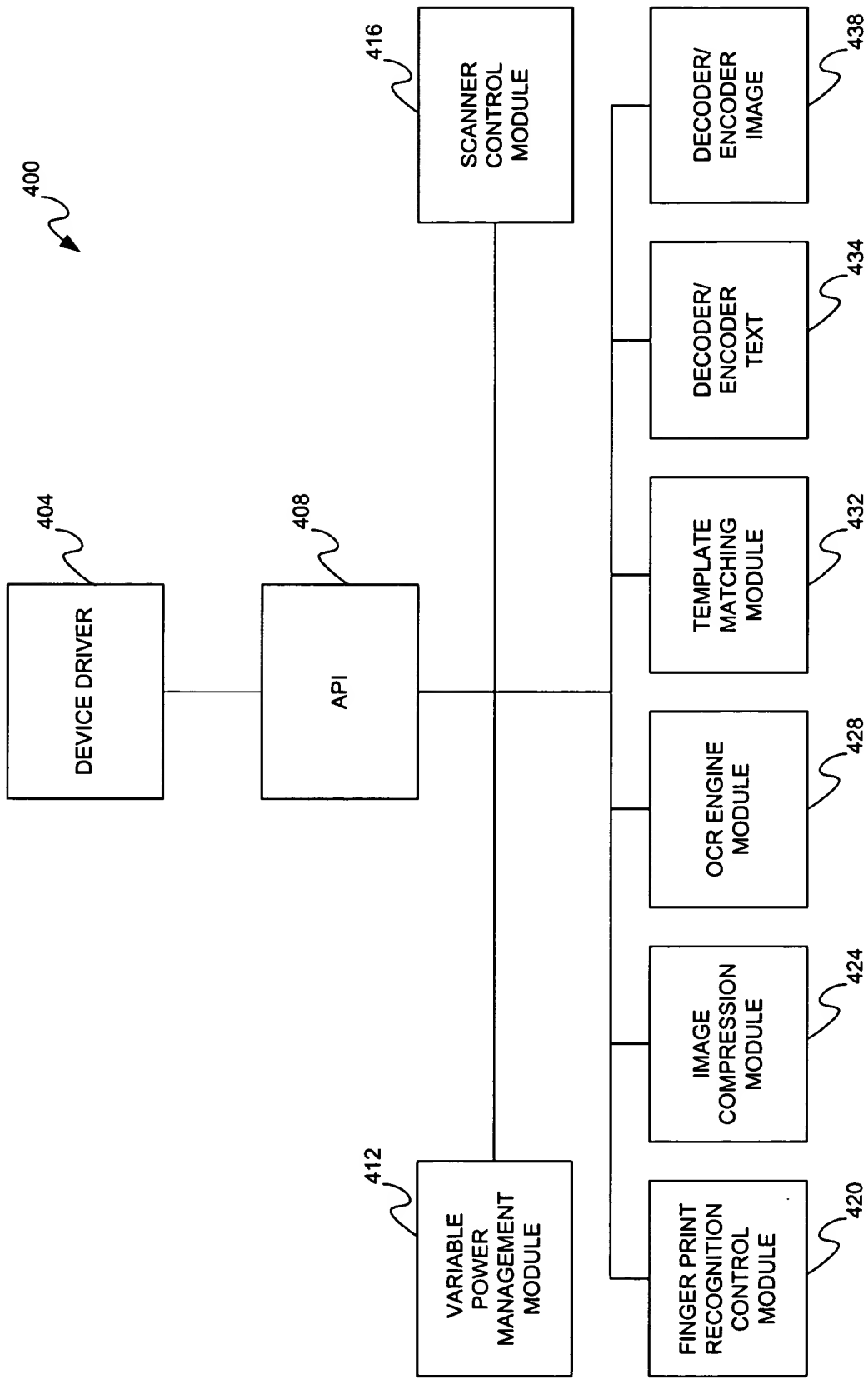
**Figure 2B**

FIG. 3 is a block diagram of a system 300. The system 300 includes a scanner 304, an ARM processor 308, an LCD display 312, a user interface 316, a power management unit 320, a memory unit 324, a power supply unit 328, and a transceiver 332. The scanner 304, ARM processor 308, LCD display 312, and user interface 316 are connected to a central bus. The power management unit 320, memory unit 324, power supply unit 328, and transceiver 332 are also connected to the central bus.

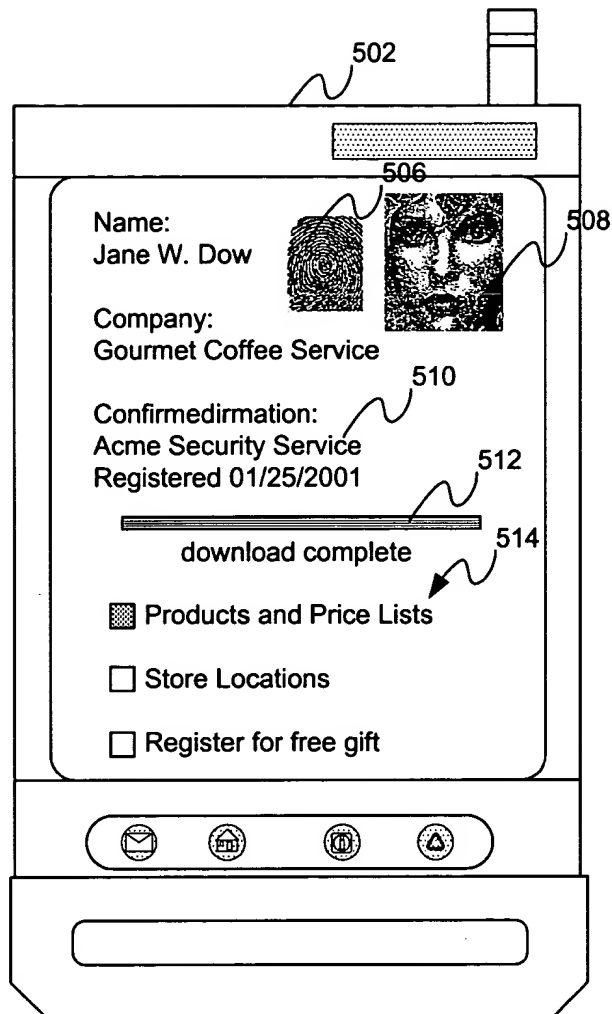


**Figure 3**

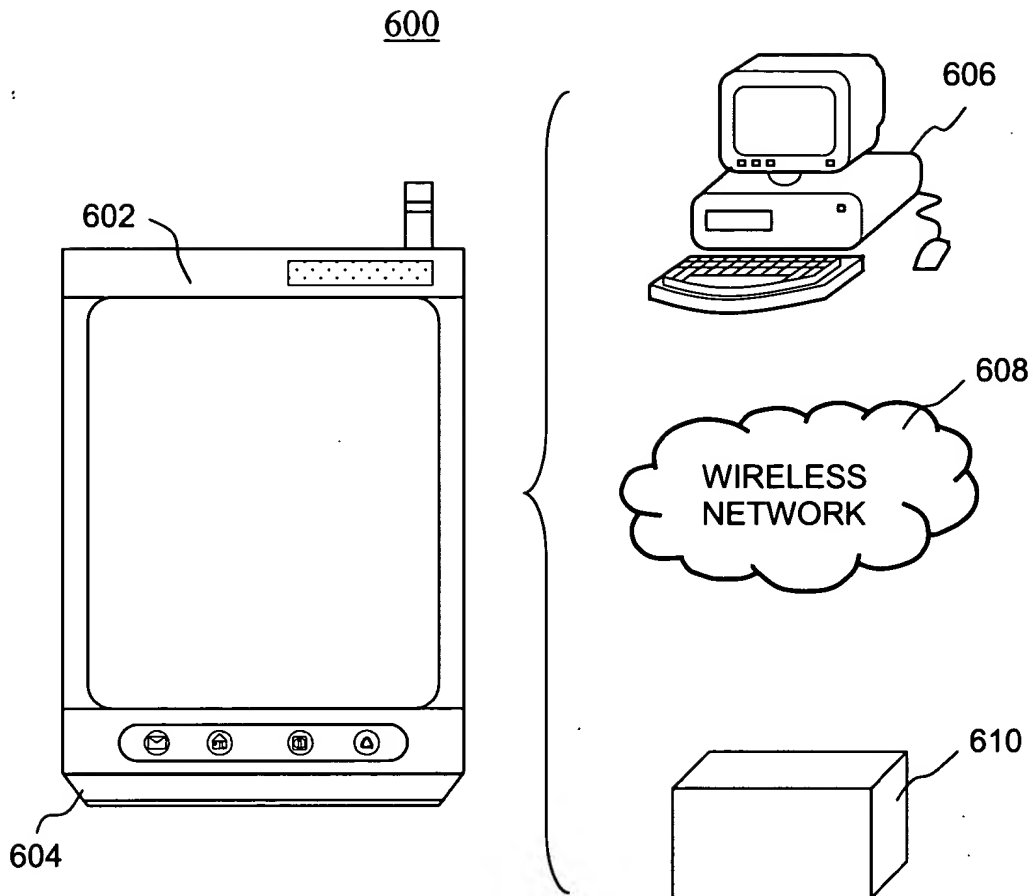
FIG. 4 is a block diagram of a system 400. The system 400 includes a device driver 404, an API 408, a scanner control module 416, a variable power management module 412, a finger print recognition control module 420, an image compression module 424, an OCR engine module 428, a template matching module 432, a decoder/encoder text module 434, and a decoder/encoder image module 438.



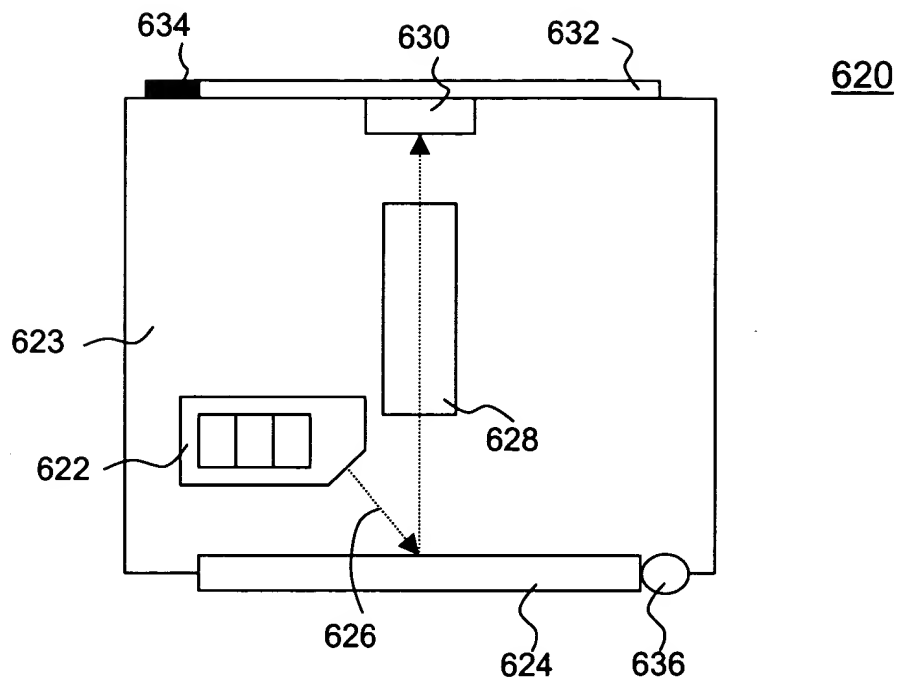
**Figure 4**



**Figure 5**

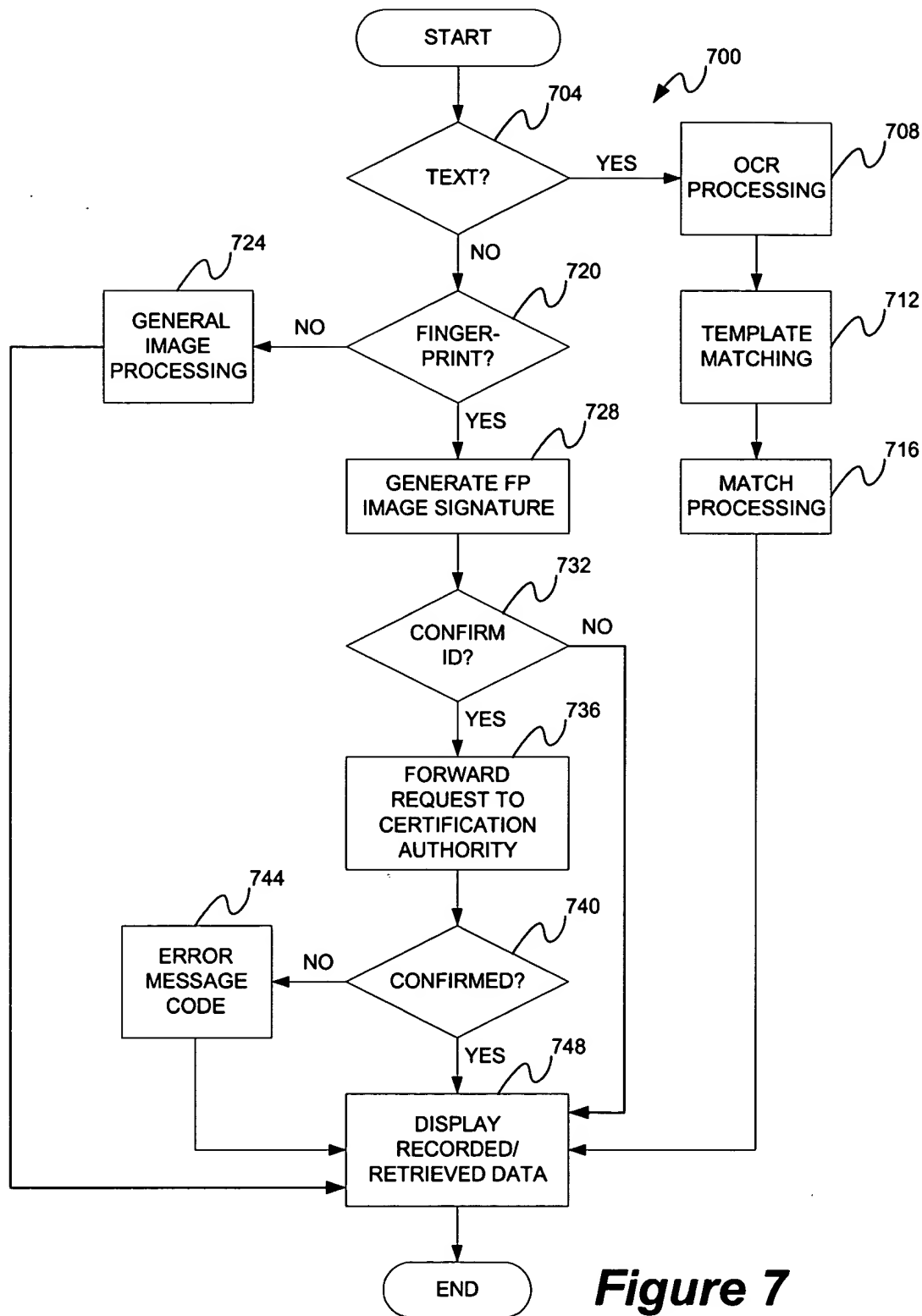


**Figure 6A**



**Figure 6B**





**Figure 7**